



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
-----------------	-------------	----------------------	---------------------	------------------

09/450,054

11/29/1999

ASHOK V. KRISHNAMOORTHY

32

7078

46363

7590

03/29/2005

MOSER, PATTERSON & SHERIDAN, LLP/
LUCENT TECHNOLOGIES, INC
595 SHREWSBURY AVENUE
SHREWSBURY, NJ 07702

EXAMINER

JACKSON, CORNELIUS H

ART UNIT

PAPER NUMBER

2828

DATE MAILED: 03/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/450,054

Applicant(s)

KRISHNAMOORTHY, ASHOK V.

Examiner

Cornelius H. Jackson

Art Unit

2828

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 17 May 2004 and 15 January 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,2,4,5,7,8,10 and 11 is/are rejected.
- 7) ☒ Claim(s) 3,6 and 9 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. In view of the Appeal Brief filed on 17 May 2004, PROSECUTION IS HEREBY REOPENED. A new ground of rejection is set forth below.
2. Acknowledgment is made that applicant's Amendment, filed on 15 January 2004, has been entered. Upon entrance of the Amendment, claims 1 and 7 were amended. Claims 1-11 are pending in the current application.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1, 2, 4, 5, 7, 8, 10 and 11 are rejected under 35 U.S.C. 102(b) as being anticipated by Trinh et al. (5132648). Regarding claim 1, Trinh et al. discloses a network **Fig. 7** for distributing a power signal in an optoelectronic circuit **14** comprising a plurality of electrically conductive pathways, **see col. 10, lines 17-34** forming at least a first level, wherein each level is comprised of a plurality of segments linearly extending

Art Unit: 2828

from a common point, each of the segments of respective levels having equal lengths, and wherein the segments of a next order higher level are formed at the extremities of a previous order lower level, the extremities of the previous order lower level functioning as the common point for the formation of the next order higher level; means for coupling **RF I/O** the power signal from a primary input to the common point of the first level; terminal nodes **CONTACT** coupled at the extremities of a last level for supplying the power signal to a plurality of devices that form at least a portion of said optoelectronic circuit **14**, wherein the number of segments connecting said primary input to each of said terminal nodes is equal such that the power supplied by the terminal nodes to each of the plurality of devices is substantially equal, **this is inherent because of the configuration of the interconnection circuit.**

Regarding claim 7, Trinh et al. discloses a network **Fig. 7** for distributing a power signal in an optoelectronic circuit **14** comprising a plurality of electrically conductive pathways, **see col. 10, lines 17-34** forming at least a first level, wherein each level is comprised of a plurality of segments linearly extending from a common point, each of the segments of respective levels having equal lengths and wherein the pathways are joined only at the extremities of a previous order lower level, the extremities of the previous order lower level functioning as the common point; means for coupling **RF I/O** the power signal from a primary input to the common point of the first level; terminal nodes **CONTACT** coupled at the extremities of a last level for supplying the power signal to a plurality of devices that form at least a portion of said optoelectronic circuit **14**, wherein the number of segments connecting said primary input to each of said

terminal nodes is equal such that the power supplied by the terminal nodes to each of the plurality of devices is substantially equal, **this is inherent because of the configuration of the interconnection circuit.**

Regarding claims 2 and 8, Trinh et al. discloses wherein each level is at least one H-shaped pattern comprising first and second parallel branches each having a respective first and second midpoint, and a third branch interconnecting said first and second midpoints, wherein the center of the H-shaped pattern is the midpoint of the third branch, **Fig. 7.**

Regarding claim 4, Trinh et al. disclose the network is located on an optoelectronic chip, **Fig. 1.**

Regarding claim 5, Trinh et al. disclose the terminal nodes are optoelectronic devices **12, see col. 6, lines 7-12 and 44-57.**

Regarding claims 10-11, it is inherent that the device claimed operates on using method claimed, therefore the rejection of the device applies also for the method.

Allowable Subject Matter

5. Claims 3, 6 and 9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

6. The following is a statement of reasons for the indication of allowable subject matter: Regarding claims 3 and 9, prior art fail to teach the claimed invention wherein each level is at least one X- shaped pattern comprising first and second branches with terminal nodes coupled at the extremities of a last level for supplying the power signal to *a plurality of devices* that form at least a portion of said *optoelectronic circuit*, wherein the number of segments connecting said primary input to each of said terminal nodes is equal *such that the power supplied by the terminal nodes to each of the plurality of devices is substantially equal*. Regarding claim 6, prior art fail to teach the plurality of devices coupled to the terminal nodes are VCSELS.

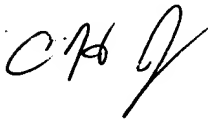
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cornelius H. Jackson whose telephone number is (571)272-1942. The examiner can normally be reached on 8:00 - 5:00, Monday - Friday.

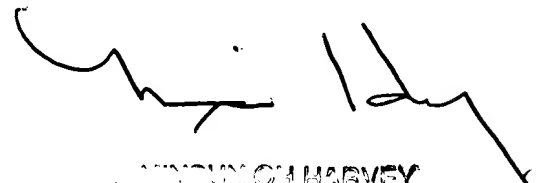
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, MinSun Harvey can be reached on (571)272-1835. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2828

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



chj



WILLIAM O. HARVEY
PATENT EXAMINER